

- EFD20 Package in SMD
- Operating Temperature range: -40°C to +125°C
- Reflow Condition : Maximum reflow temperature is 250°C/-5°C, time is 20~40 sec. (Refer to IPC/ JEDEC J-STD-020C standard).



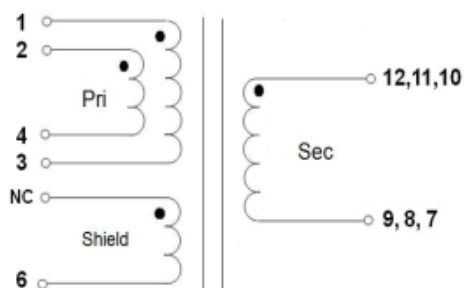
**Electrical Specifications @ 25°C**

Inductance Lp @200KHz, 0.1V (1,2 - 3,4)	Leakage Inductance Ls @200KHz, 0.1V (tie 7+8+9+10+11+12) (1,2 - 3,4)	Turn Ratio @10KHz, 0.1V (Un-gapped Core) (1 - 3) : (2 - 4)	Turn Ratio @ 10KHz, 0.1V (Un-gapped Core) (1,2 - 3,4) : (12,11,10 7 9,8,7)
9.0uH ( 10%)	0.45uH Max	1 : 1 (±3%)	0.889:1 (±3%)

**Electrical Specifications @ 25°C - Continue**

DCR (1,2 - 3,4)	DCR (12,11,10 - 9,8,7)	Hi-Pot @6sec Pri to Sec	Hi-Pot @6sec (Pri & Sec) to 6	Hi-Pot @6sec (Pri & Sec) to Core
18mΩ max	36mΩ max	1800Vac	600Vac	600Vac

**Schematic**

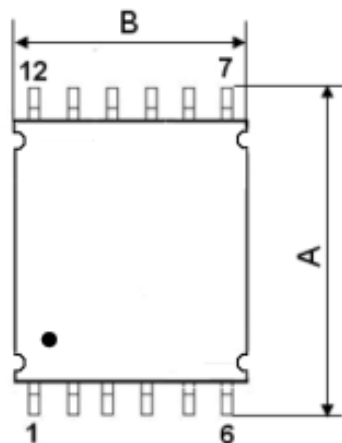
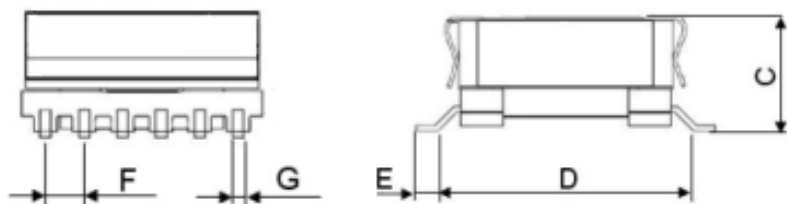


**Marking**



YY- Year; WW-Week; X - Supplier code

**Mechanicals:**



A	29.50 Max
B	22.00 Max
C	12.00 Max
D	24.60 Ref
E	2.00 Ref
F	3.00 ± 0.20
G	0.70 ± 0.10
Unit : mm	

Rev	Date	Description	Who
A	Jun 10	New Release	JS Hill